## **REMARKS**

Claims 1-6 remain the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding objections and rejections are respectfully requested in light of the above amendments and following remarks.

The Examiner alleges that Figures 7 and 8 should be designated as prior art.

Applicants respectfully traverse this allegation.

The Examiner's attention is directed to pages 6 and 7 of the instant application where Figures 7 and 8 are described as part of the present invention.

Turning now to the art rejection, Claims 1-6 were rejected under 35 U.S.C. § 103 as being unpatentable over Contreras in view of Mohsen.

These rejections are respectfully traversed.

It is respectfully submitted that Contreras does not disclose or suggest the presently claimed invention including the second row of connection points positioned along at least one edge of the communication chip and behind the first row.

Applicants agree with the Examiner as evidenced by page 3 of the Office Action that Contreras fails to explicitly disclose the arrangement of the connection points in a form of the first row and the second row.

It is respectfully submitted that Mohsen does not disclose or suggest the presently claimed invention including the second row of the connection points positioned along at least one edge of the communication chip and behind the first row.

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Mohsen is directed to a custom tooled printed circuit board.

In light of the above, it is respectfully submitted that the presently claimed invention is patentable over the applied references.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,

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